

FIG. 1

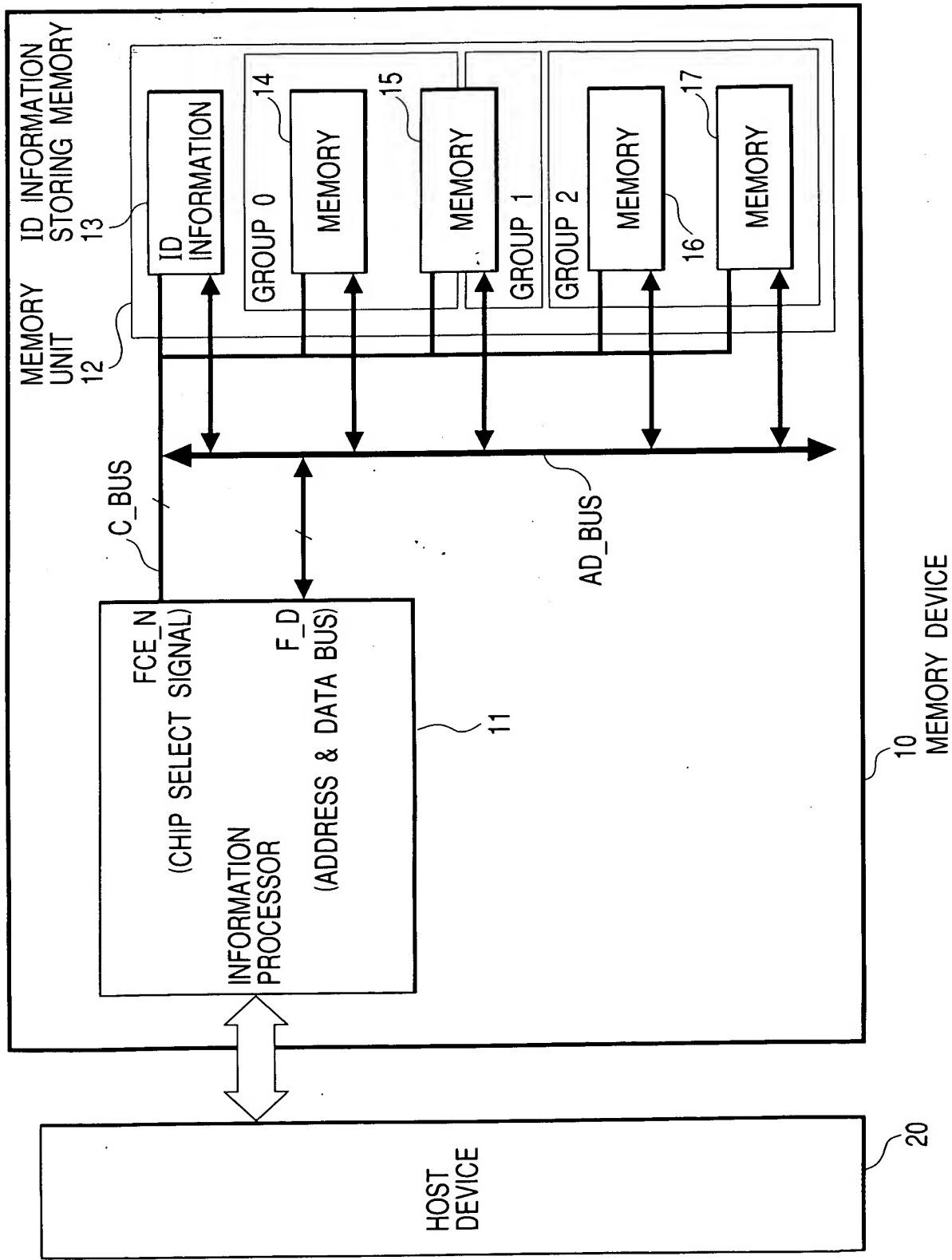
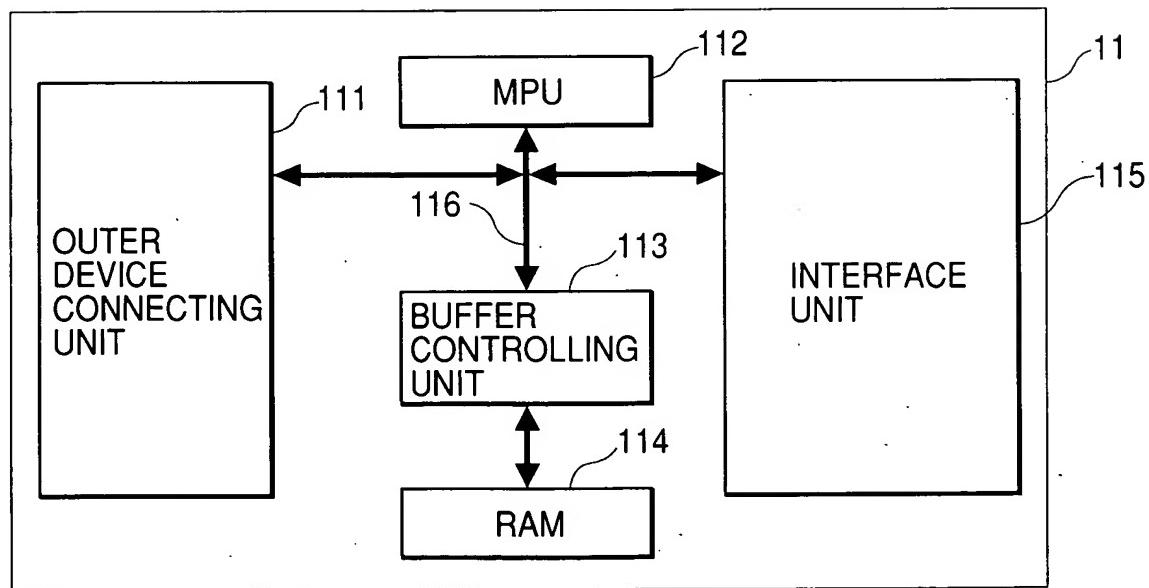


FIG. 2

20190726094250000000

FIG. 3(A)

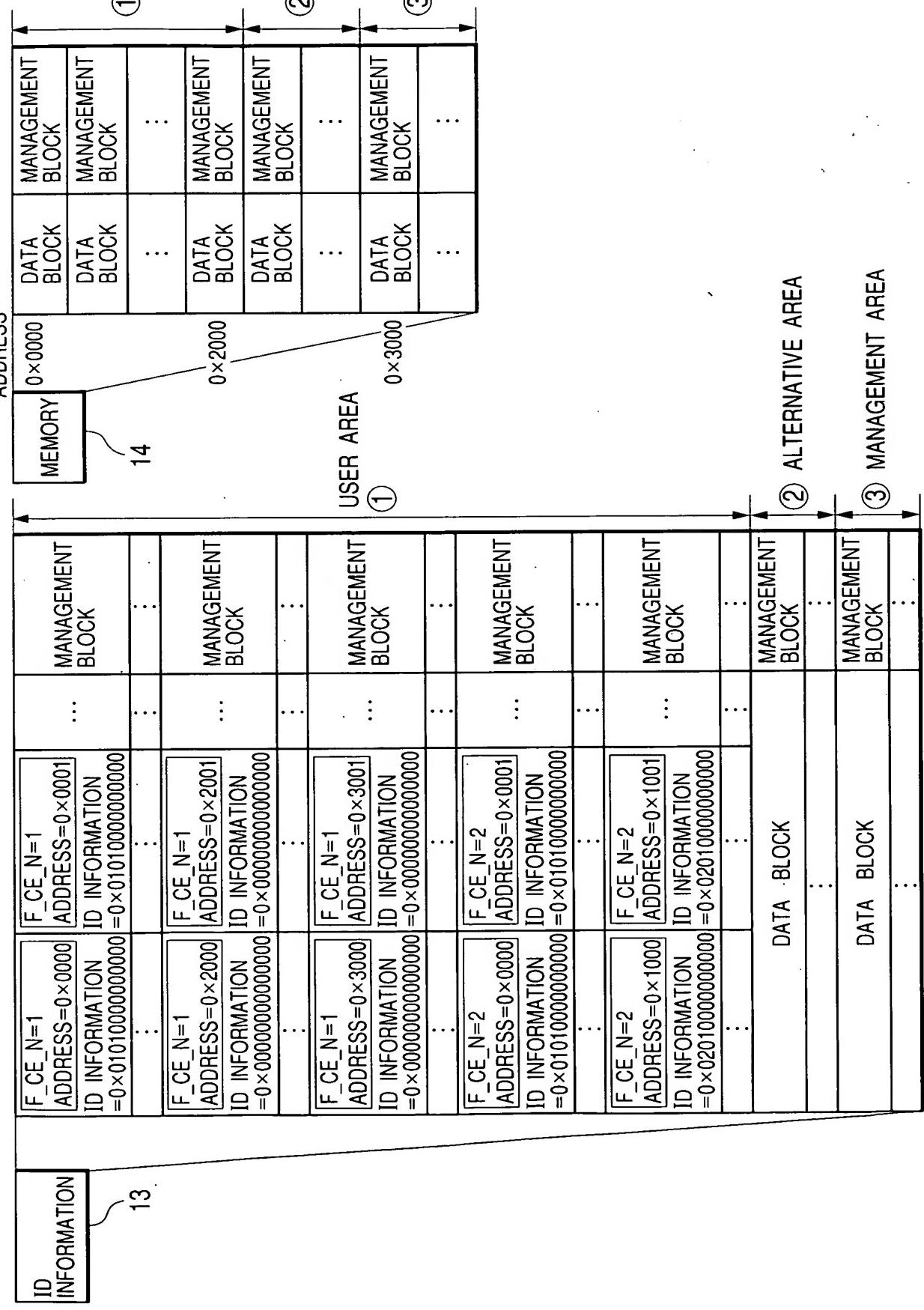


FIG. 3(B)

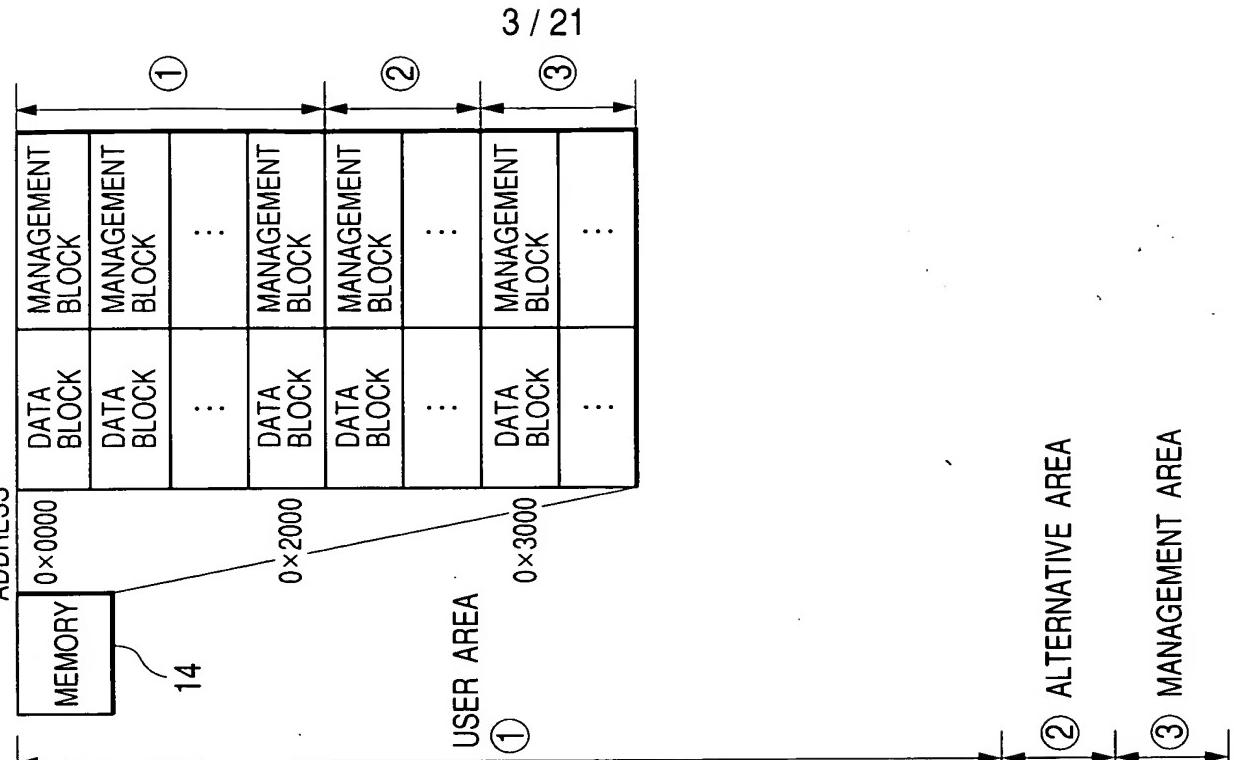
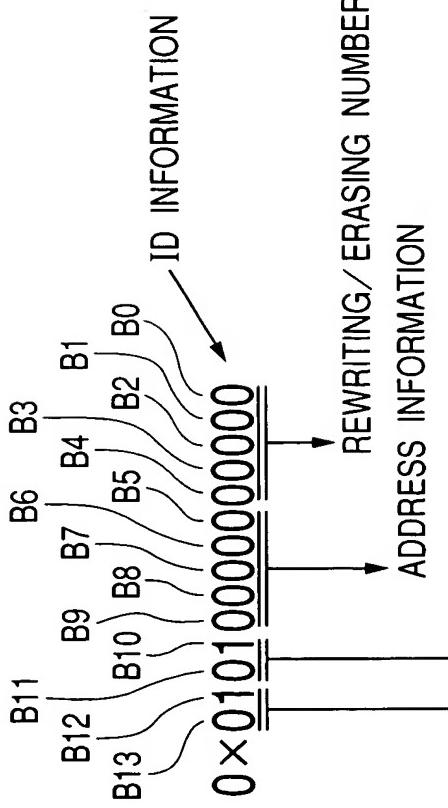


FIG. 4

F_CE_N	ADDRESS (F_D)	ID INFORMATION
1	0×0000	0×0101000000000000
⋮	⋮	⋮
1	0×1FFF	0×0101000000000000
2	0×0000	0×0101000000000000
⋮	⋮	⋮
2	0×OFFF	0×0101000000000000
2	0×1000	0×0201000000000000
⋮	⋮	⋮
2	0×1FFF	0×0201000000000000
3	0×0000	0×0400100000000000
⋮	⋮	⋮
4	0×1FFF	0×040021FFF00000

↑
GROUP 0
↓
GROUP 1
↓
GROUP 2
↓

NORMAL READ/WRITE
 READ ONLY
 MIRRORING (TIME DIFFERENCE)

FIG. 5

PRELIMINARY AREA INFORMATION

(bit)	MIRRORING (TIME DIFFERENCE)	MIRRORING (SIMULTANEOUS)	SPARE	SPARE PRIORITY	SPARE PROCESSING
0000_0000	NO	NO	NO	—	—
0000_0001	YES	NO	NO	—	—
0000_0010	NO	YES	NO	—	—
0000_0100	NO	NO	YES	—	—
0000_0111	YES	YES	YES	NO	—
0000_1111	YES	YES	YES	YES	—
0000_0110	NO	YES	YES	NO	—
0000_1110	NO	YES	YES	YES	—
:	:	:	:	:	:
1111 xxxx	—	—	—	—	FINISH

(bit)	CONTROLLING CONTENTS
0000_0001	NORMAL READ/ WRITE AREA
0000_0010	READ ONLY (WRITE PROTECT) AREA
0000_0011	WRITE ONLY AREA
0000_0100	MIRRORING (TIME DIFFERENCE) AREA
0000_1000	MIRRORING (SIMULTANEOUS) AREA
1111 xxxx	SPARE AREA
:	:

FIG. 6

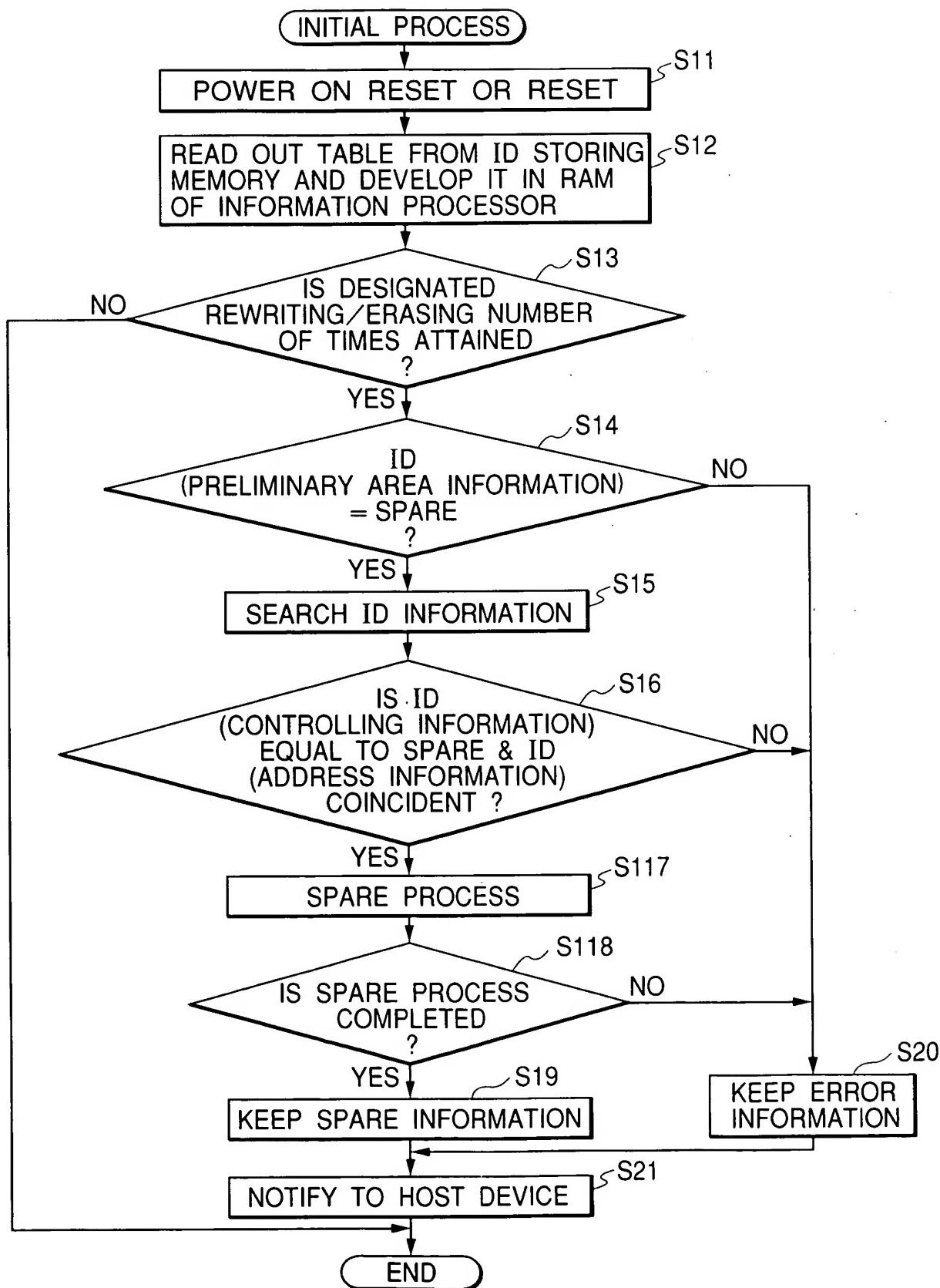


FIG. 7

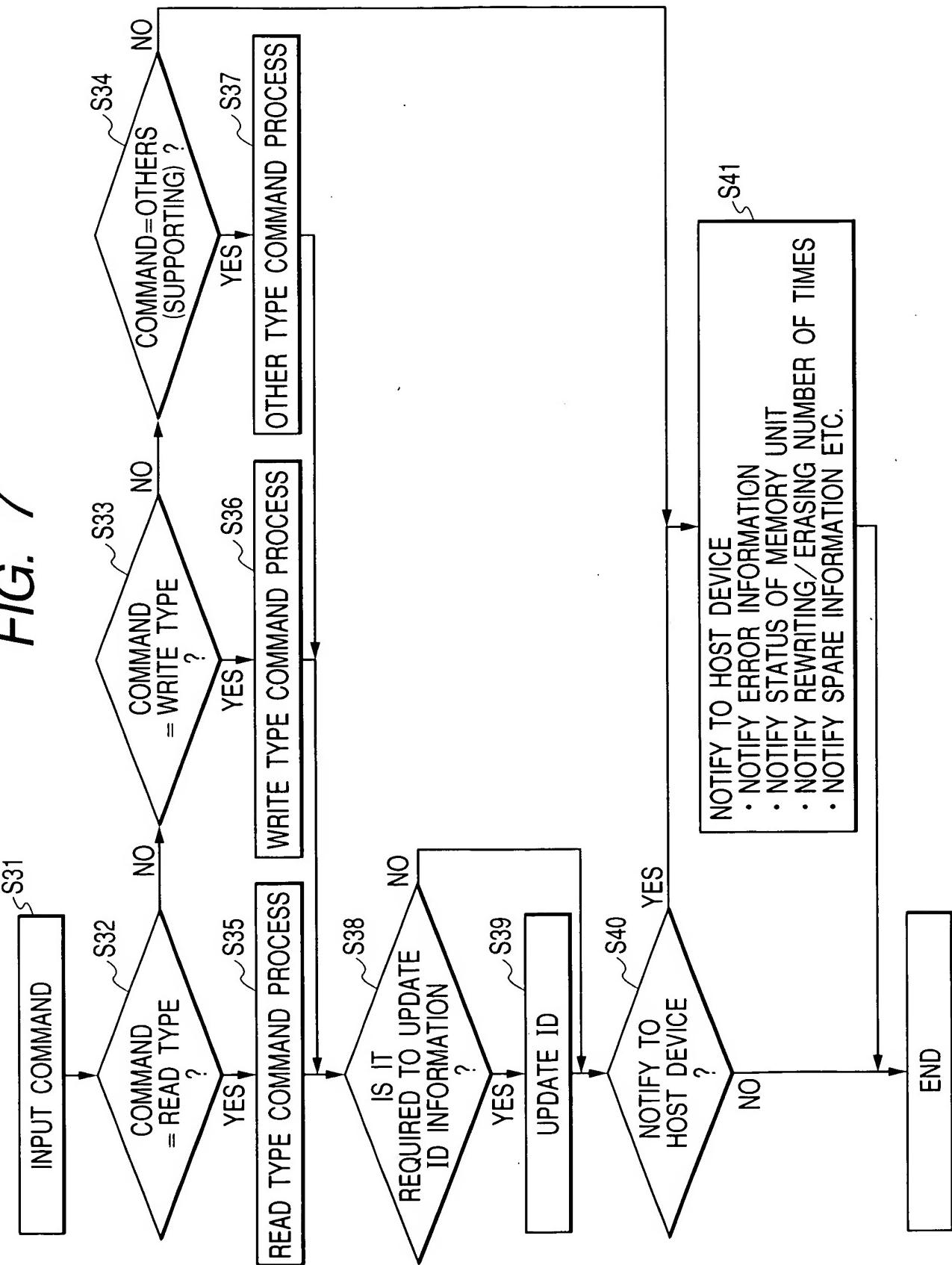


FIG. 8

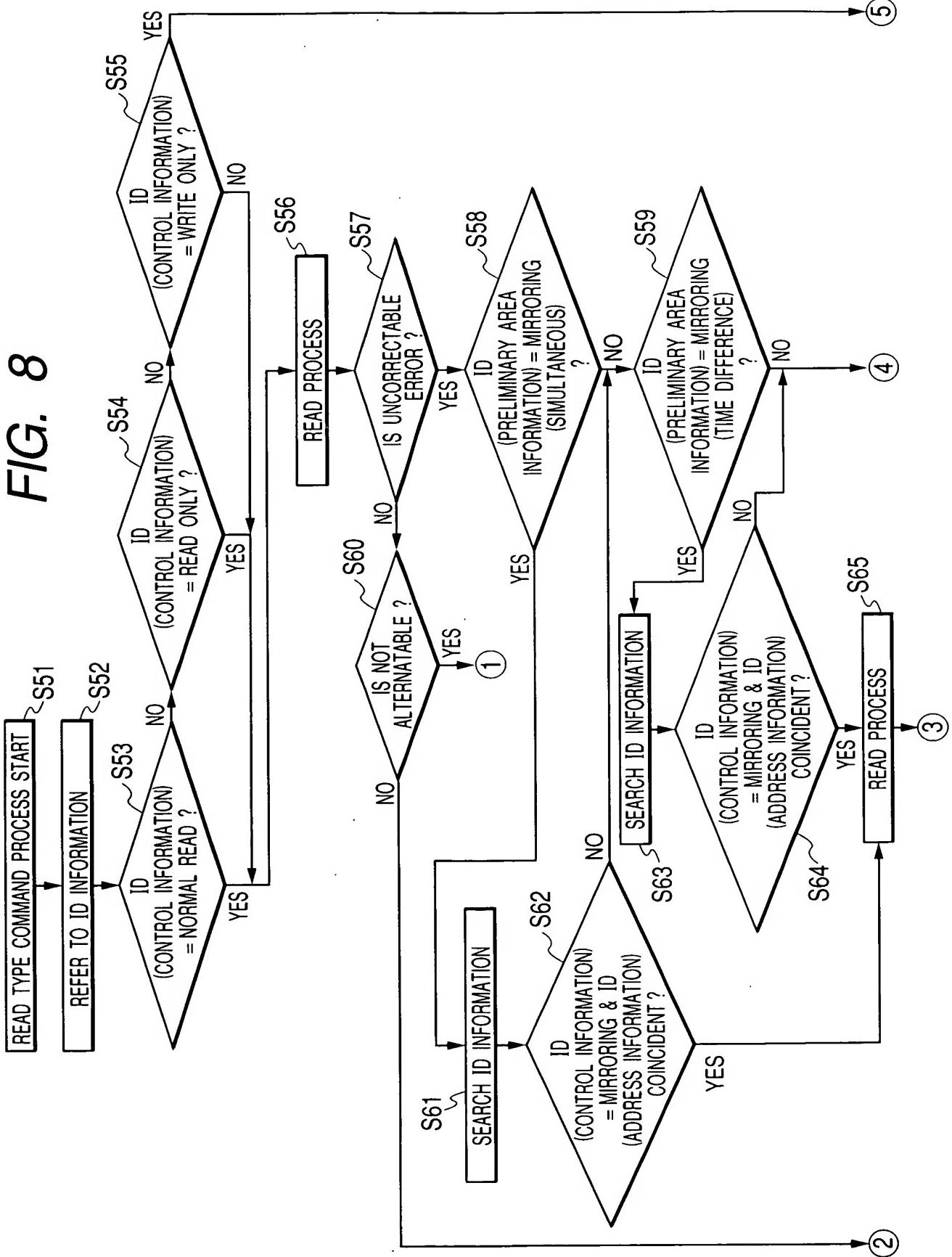


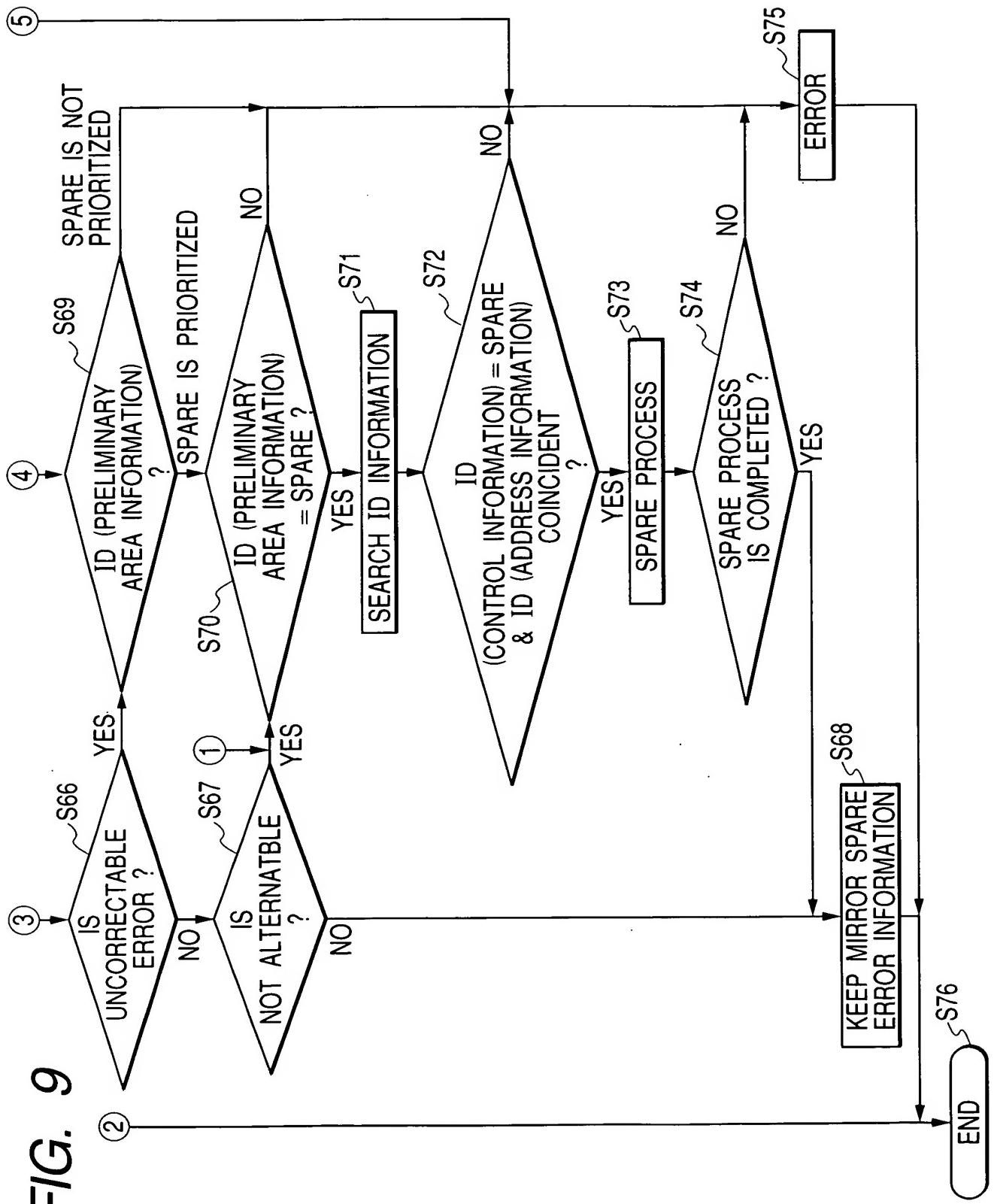
FIG. 9

FIG. 10

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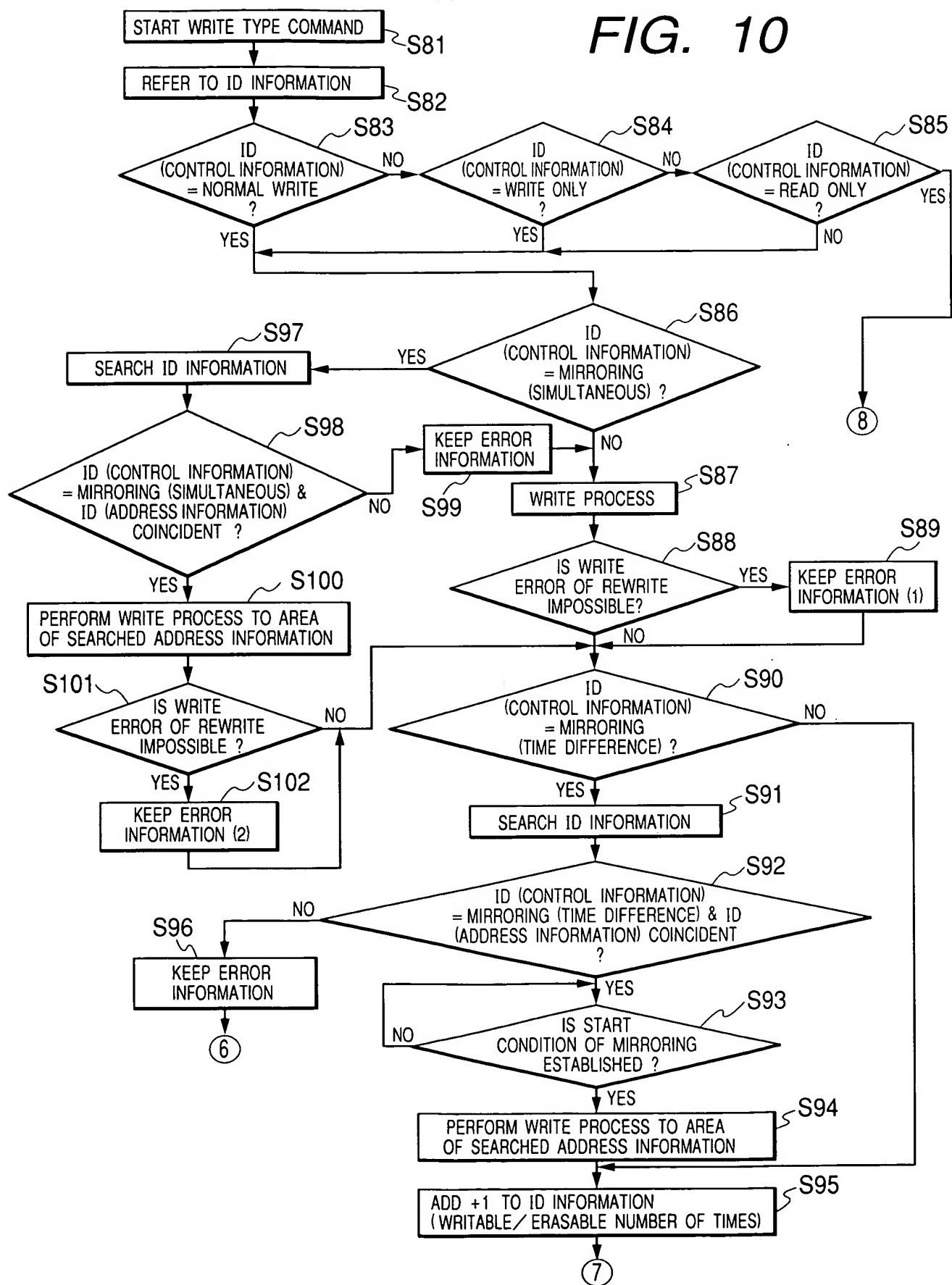


FIG. 11

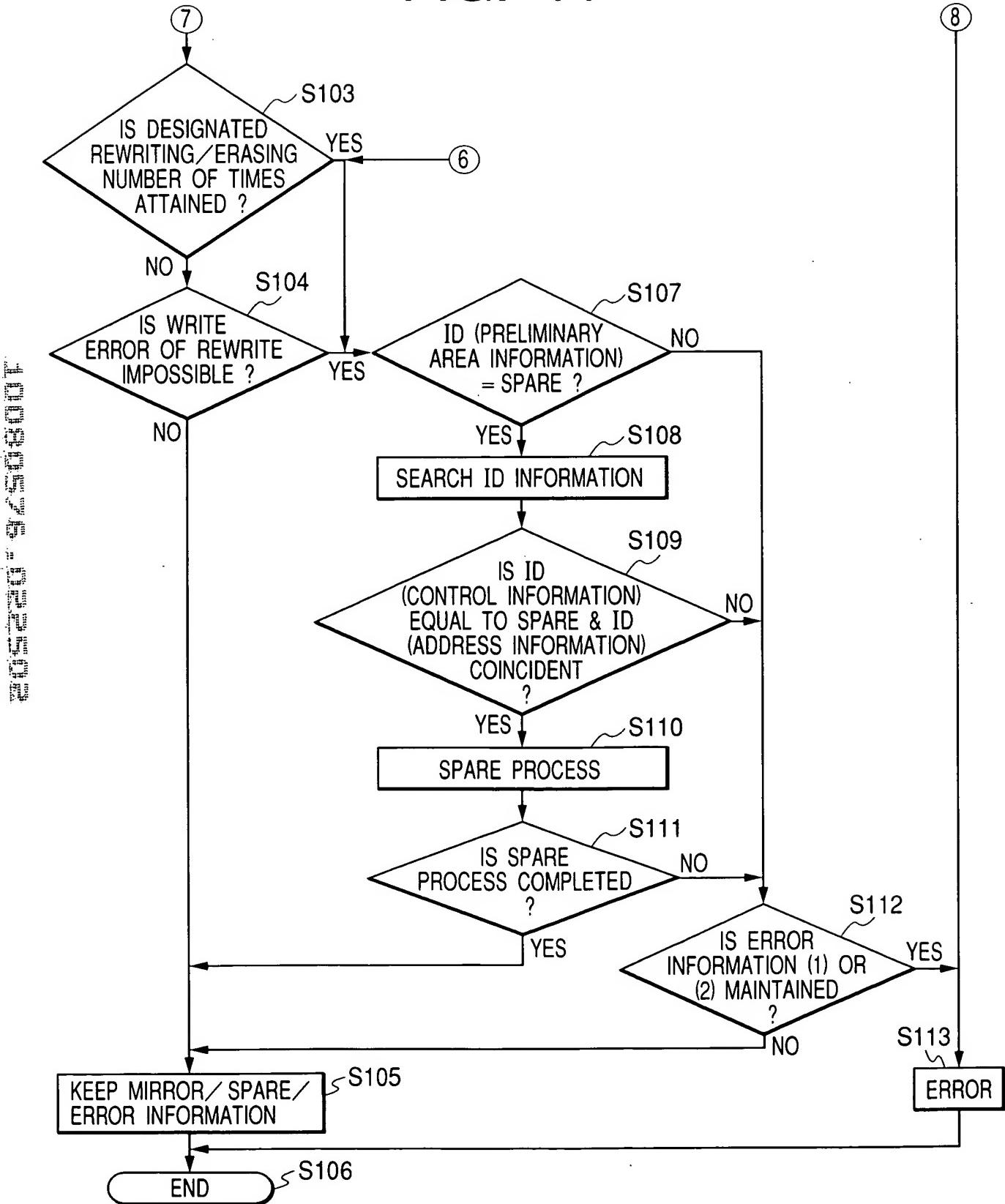


FIG. 12

REF ID: A2500007

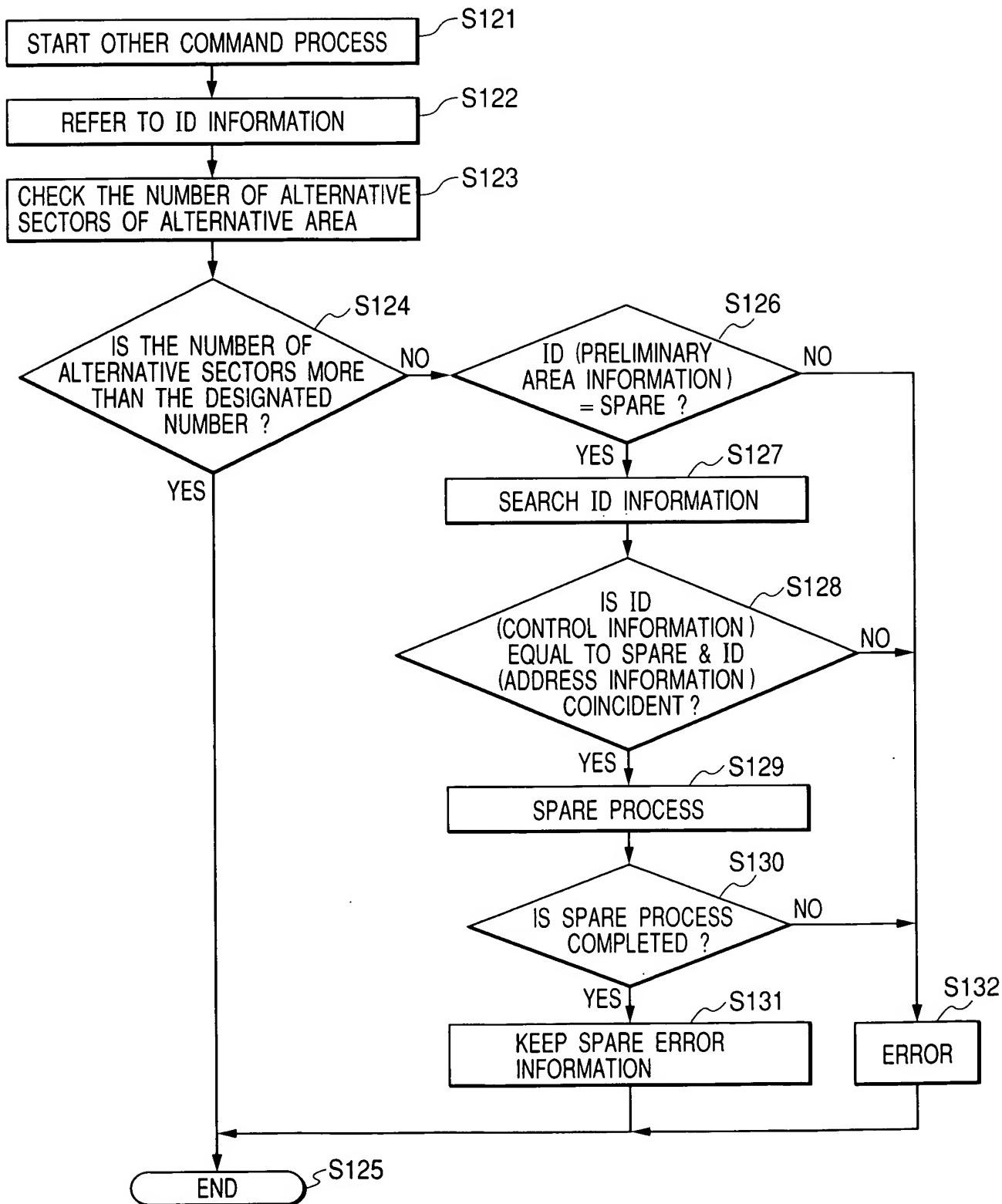


FIG. 13

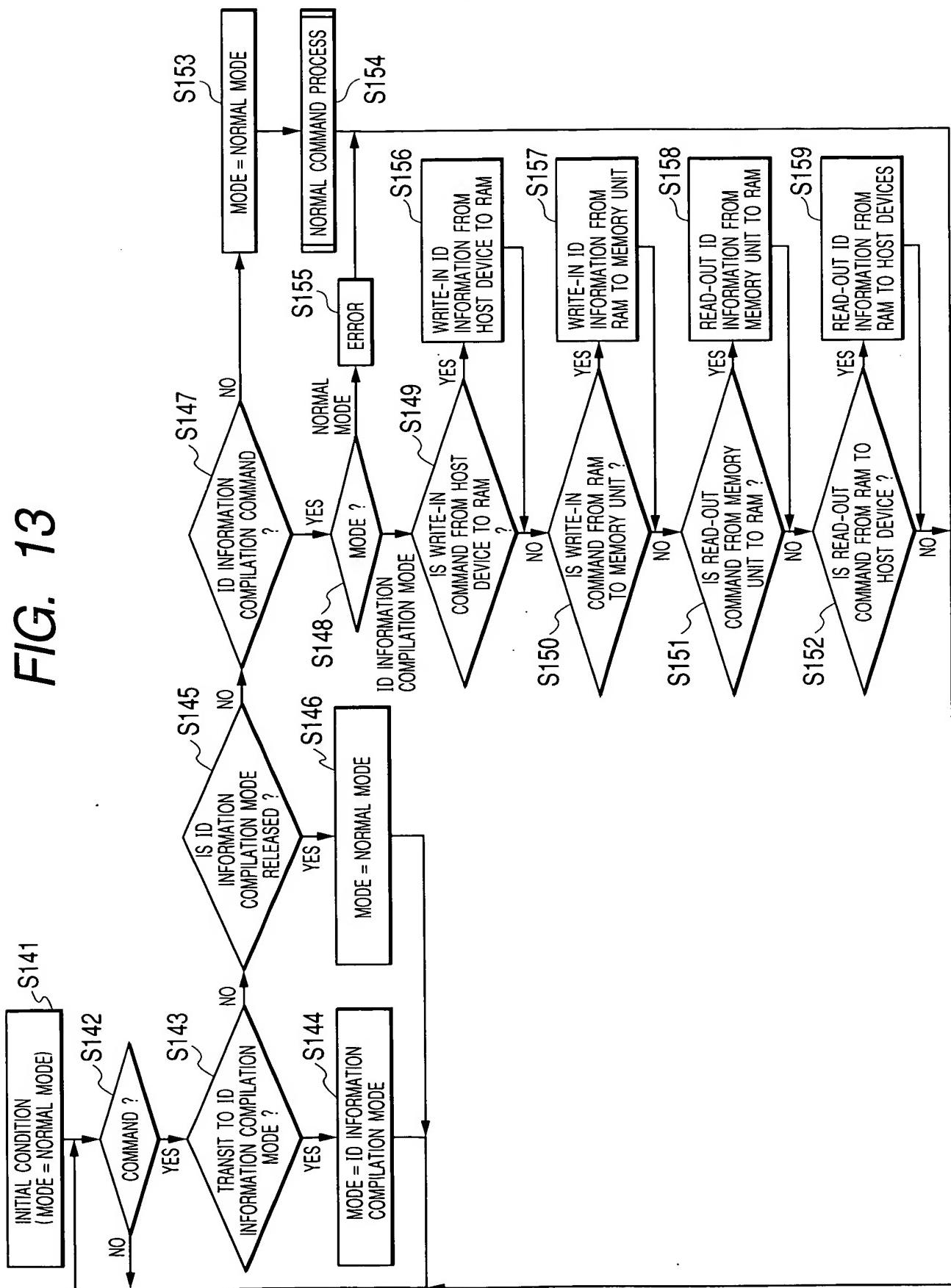


FIG. 14

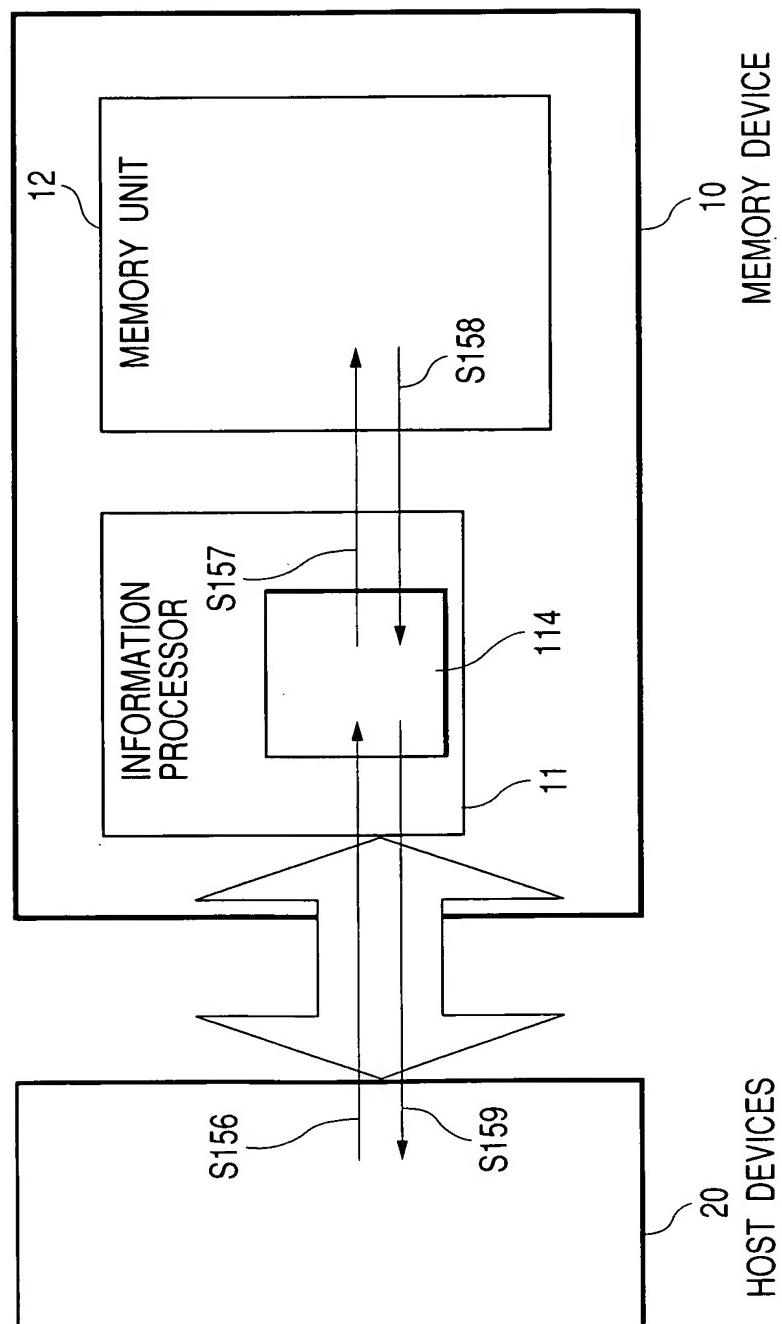


FIG. 15

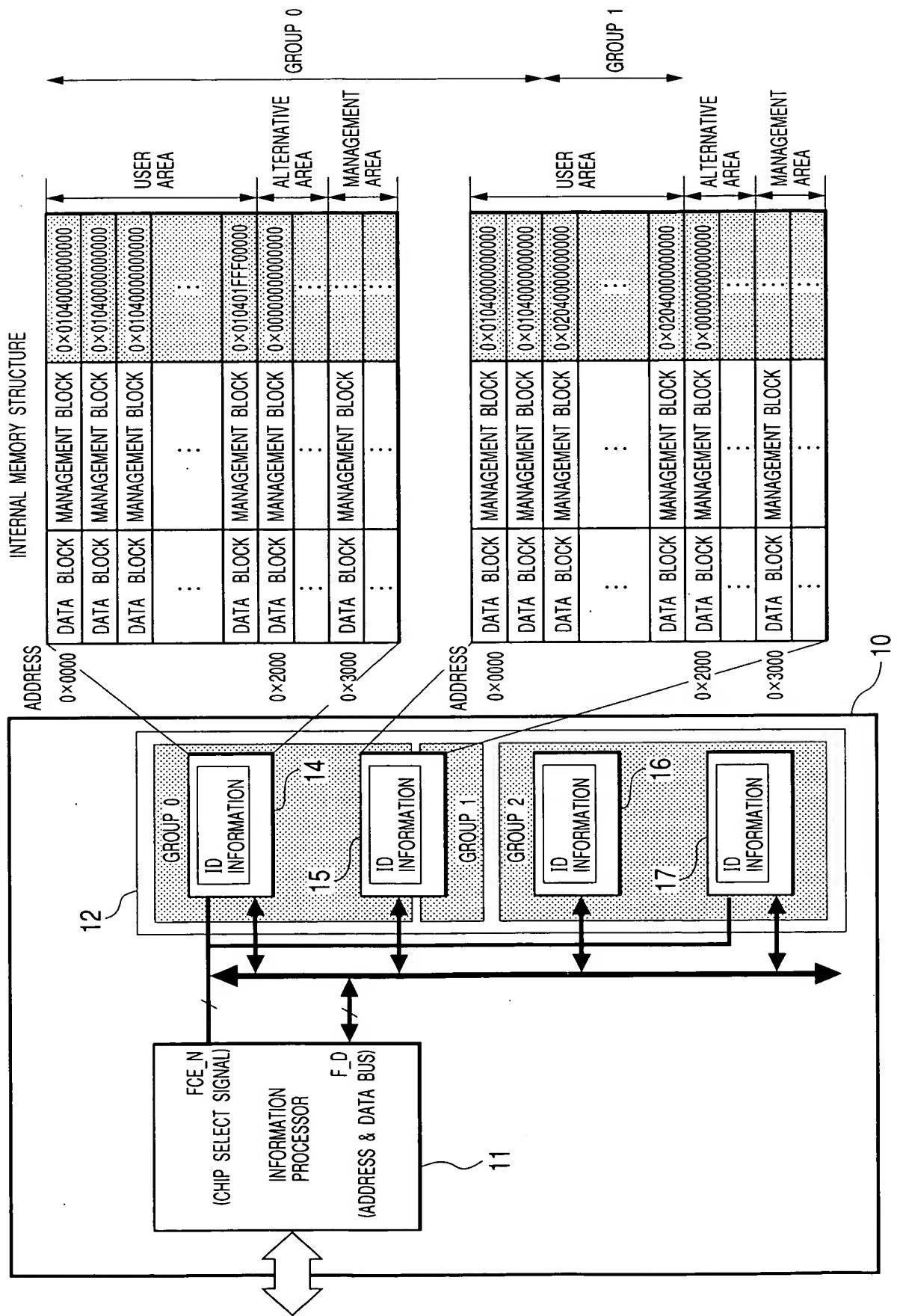


FIG. 16

TOP SECRET//COMINT

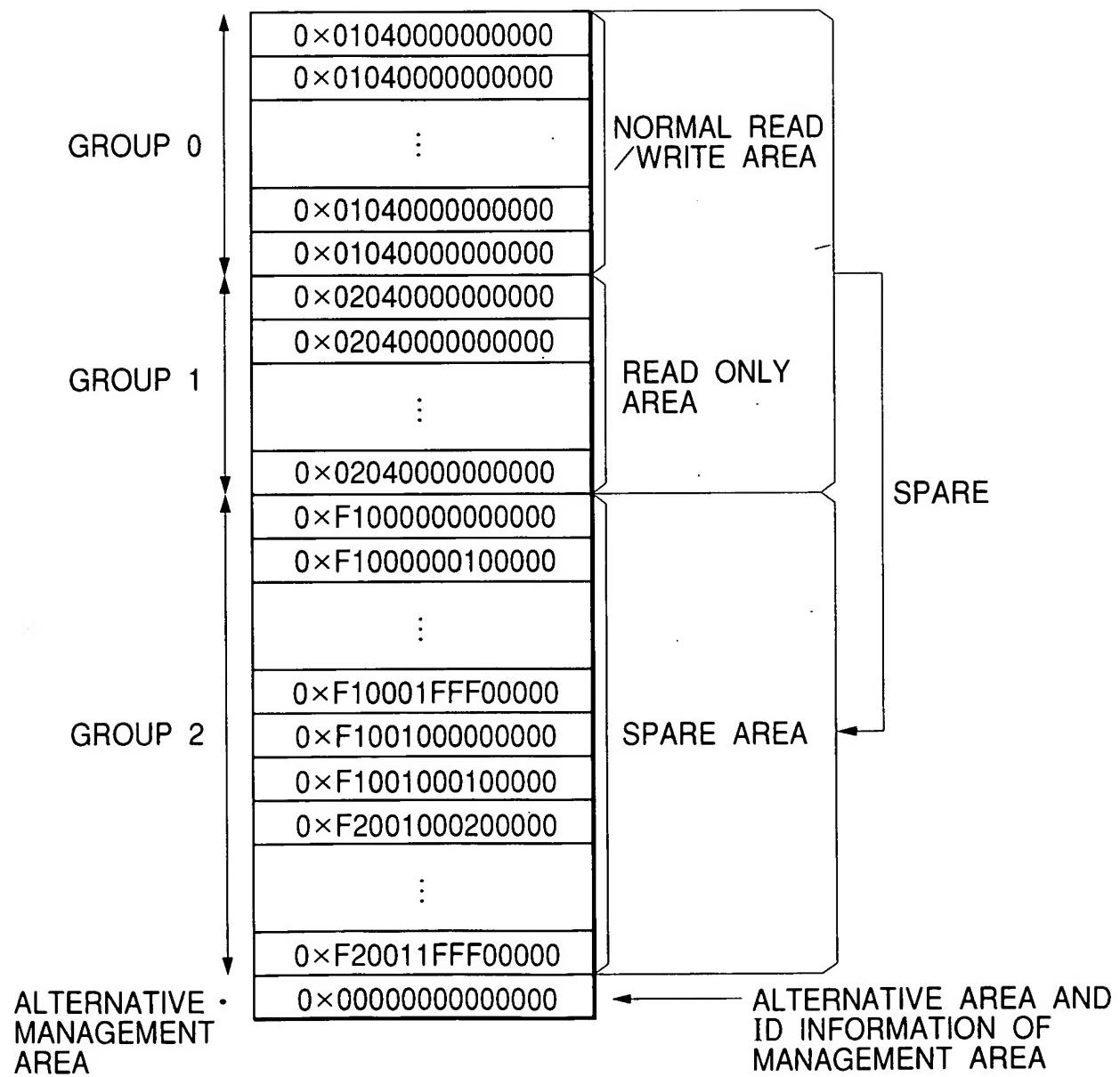


FIG. 17

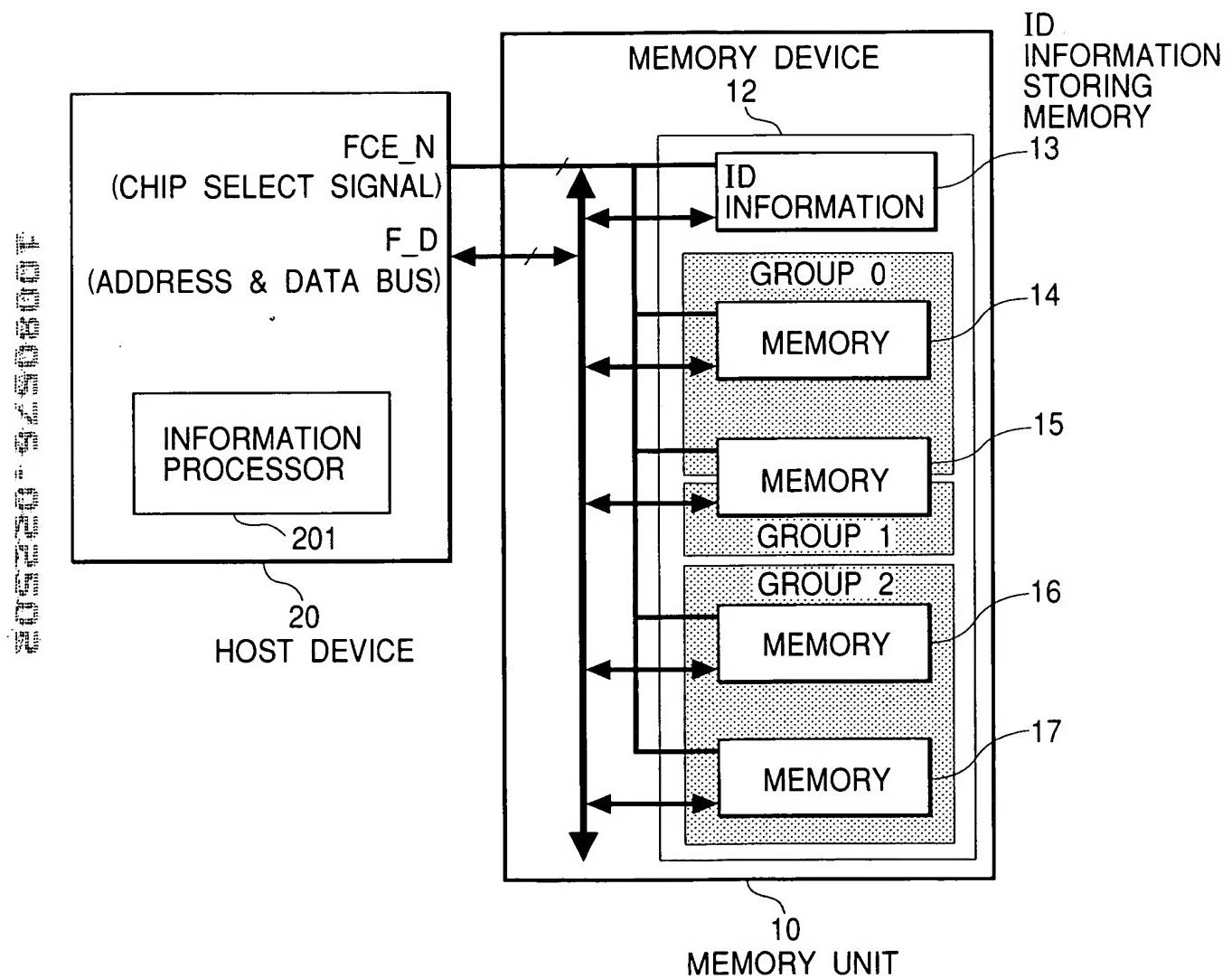


FIG. 18

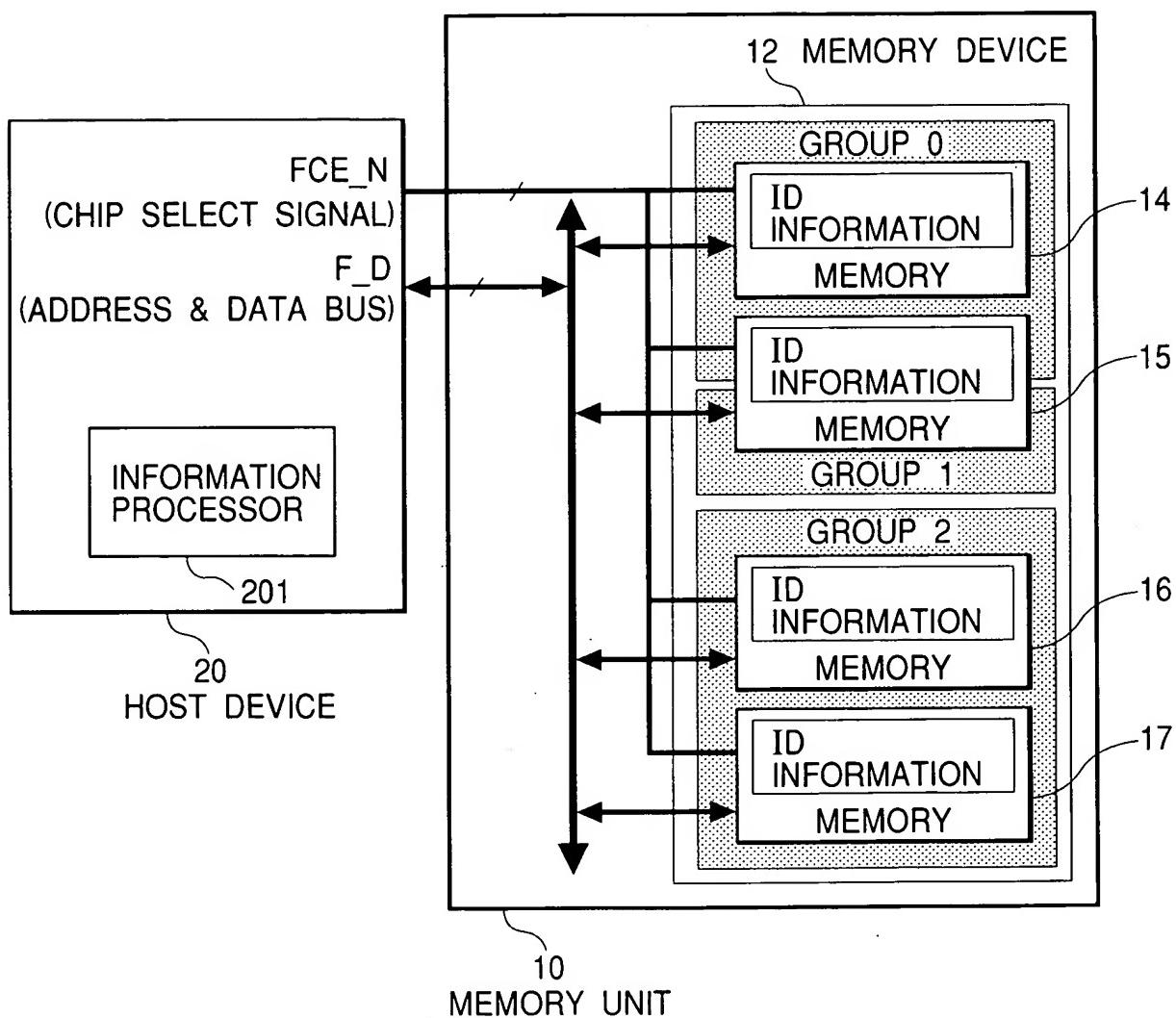


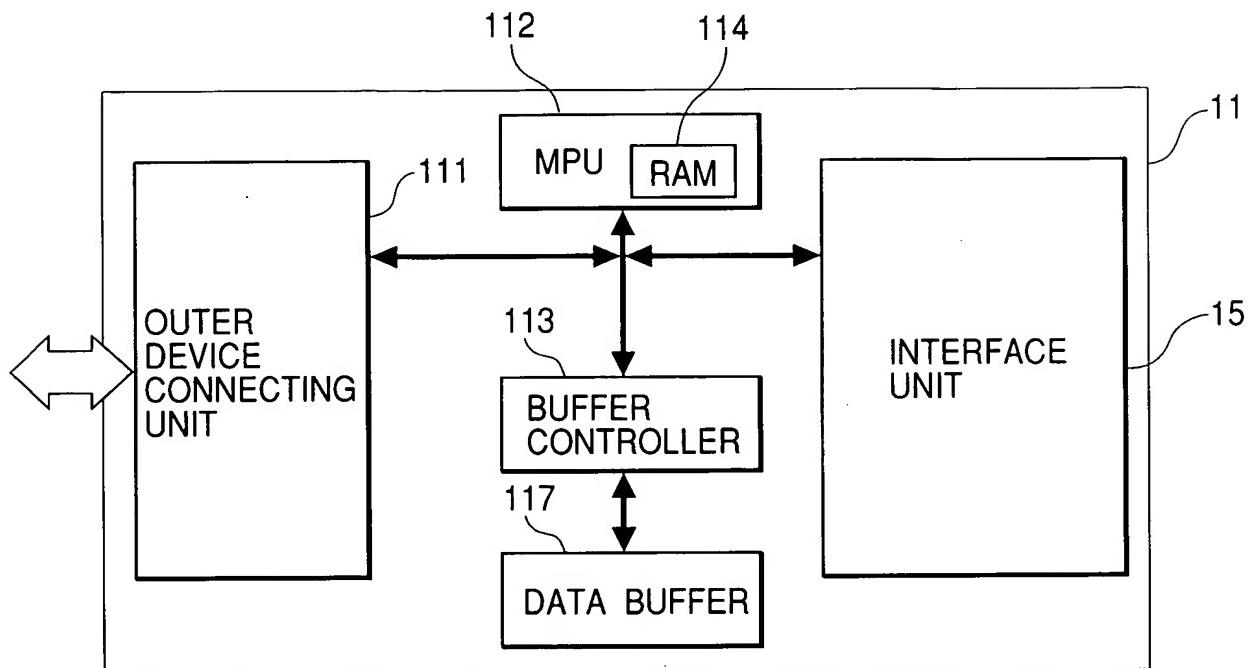
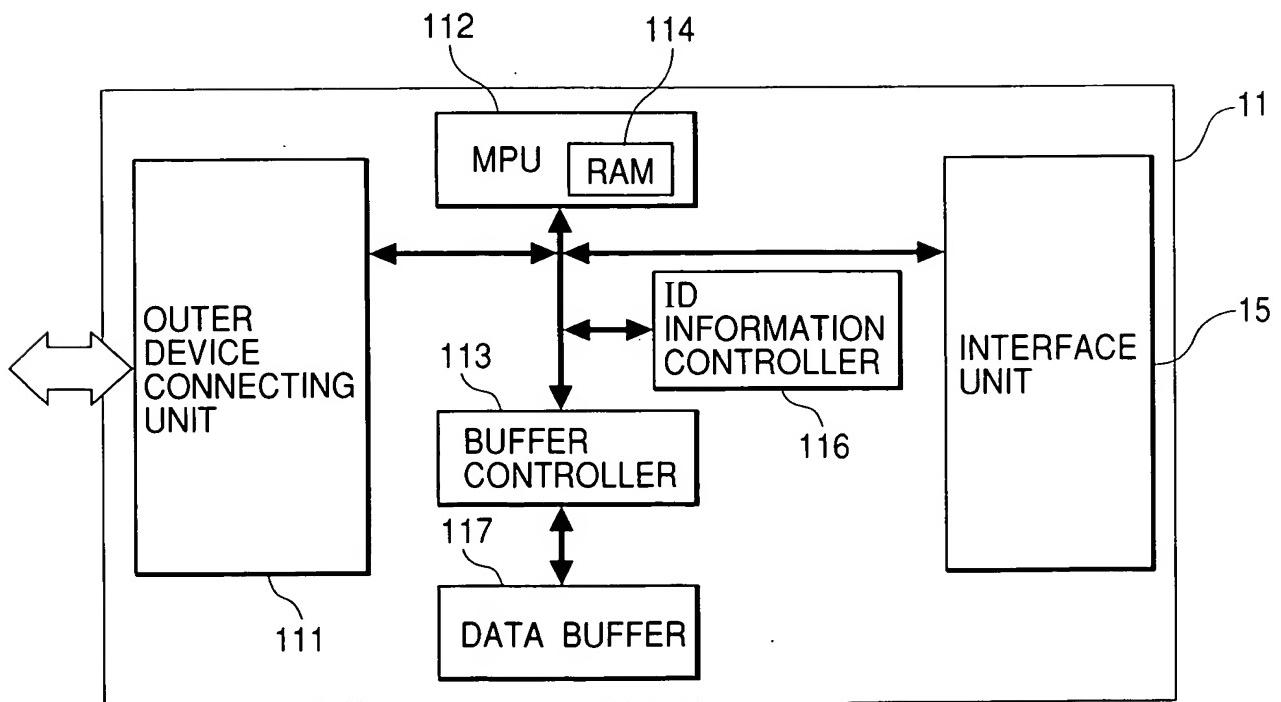
FIG. 19**FIG. 20**

FIG. 21

PROPOSED REPORT

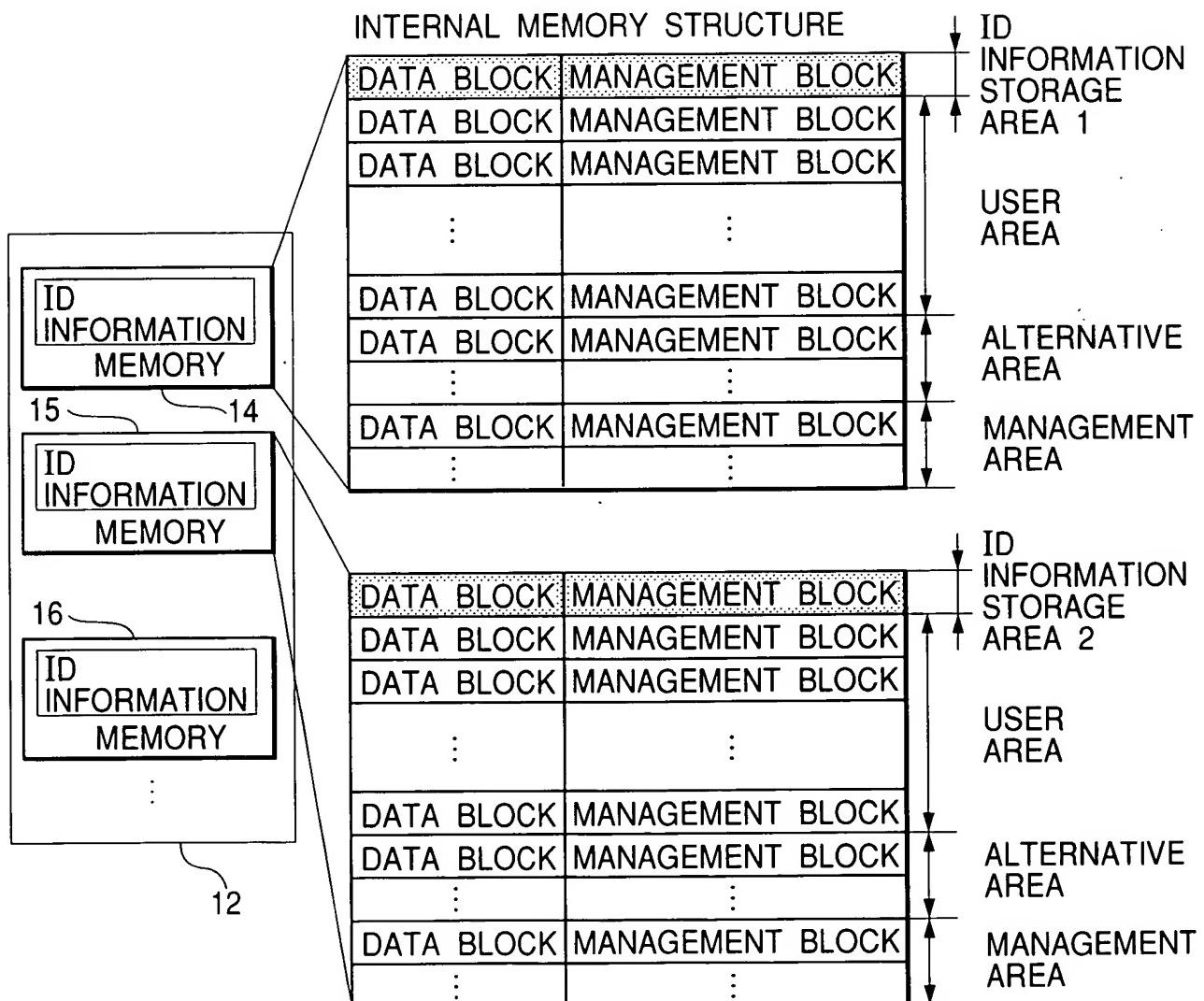


FIG. 22(A)

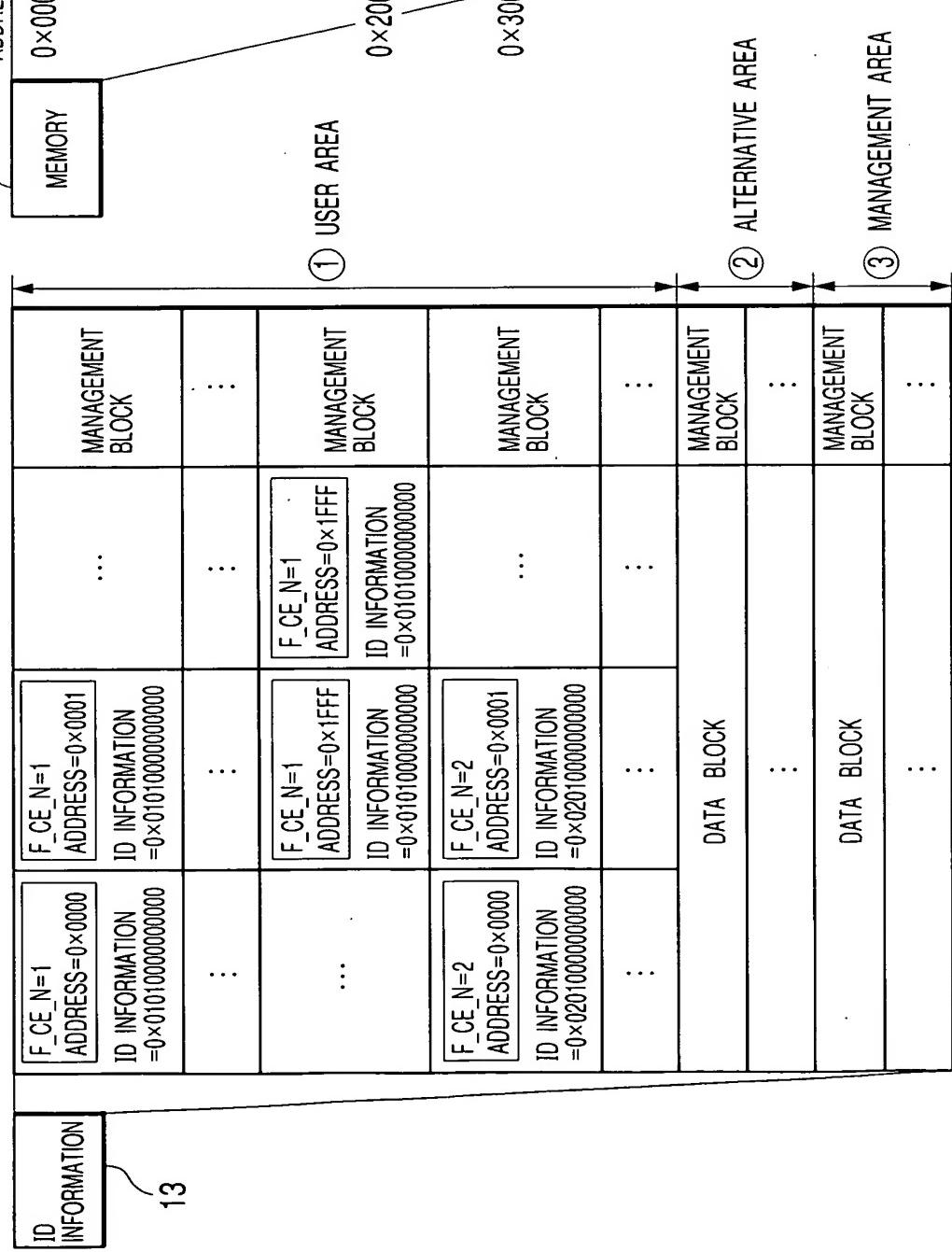


FIG. 22(B)

